

A 13.8-kV 4.75-MVA Microgrid Laboratory Test Bed

Yusi Liu,

H. Alan Mantooh,

Juan Carlos Balda

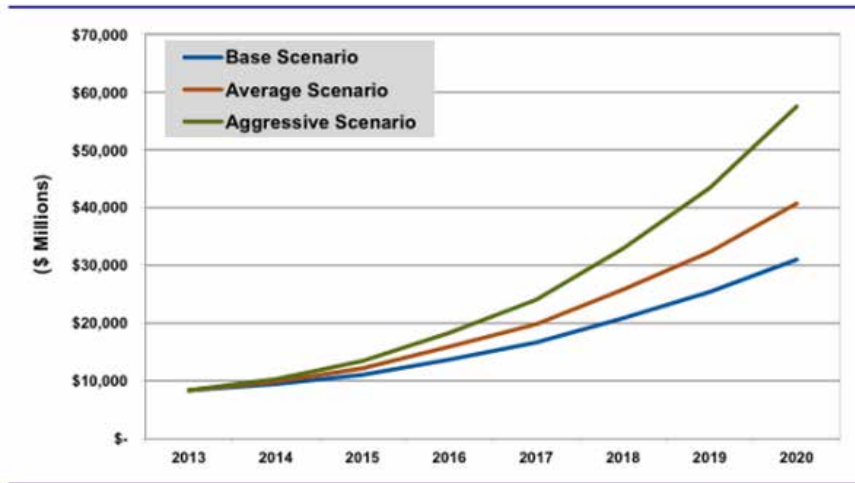
University of Arkansas

National Center for Reliable Electric Power Transmission

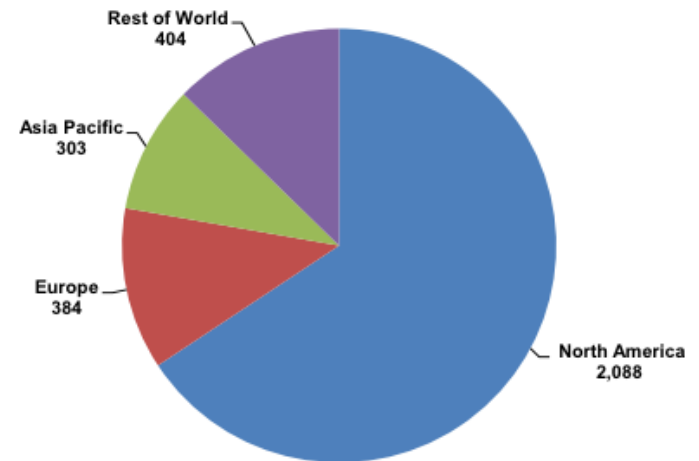
Outline

- Ø **Introduction**
- Ø **UA Microgrid Vision**
- Ø **Test Bed System**
- Ø **Purposes of the Test Bed**
- Ø **Hardware Experimental Results**
- Ø **Conclusions and Future Work**

Microgrid Market



(Source: Navigant Research)



(MW)

(Source: Pike Research)

Background and Motivation

- Integration of distributed generation (DG)
- Improve energy efficiencies and reduce emissions
- Reduce load outages



IEEE Guide for Design, Operation, and Integration of Distributed Resource Island Systems with Electric Power Systems (IEEE Std 1547.4™-2011)

Background and Motivation

- Integration of distributed generation (DG)
- Improve energy efficiencies and reduce emissions
- Reduce load outages



Outline

- Ø Introduction
- Ø **UA Microgrid Vision**
- Ø Test Bed System
- Ø Purposes of the Test Bed
- Ø Hardware Experimental Results
- Ø Conclusions and Future Work

UA Microgrid Vision

- Integration of distributed generation (DG) with power electronic interfaces – in the MVA range



(Fuel cell source: DOE)



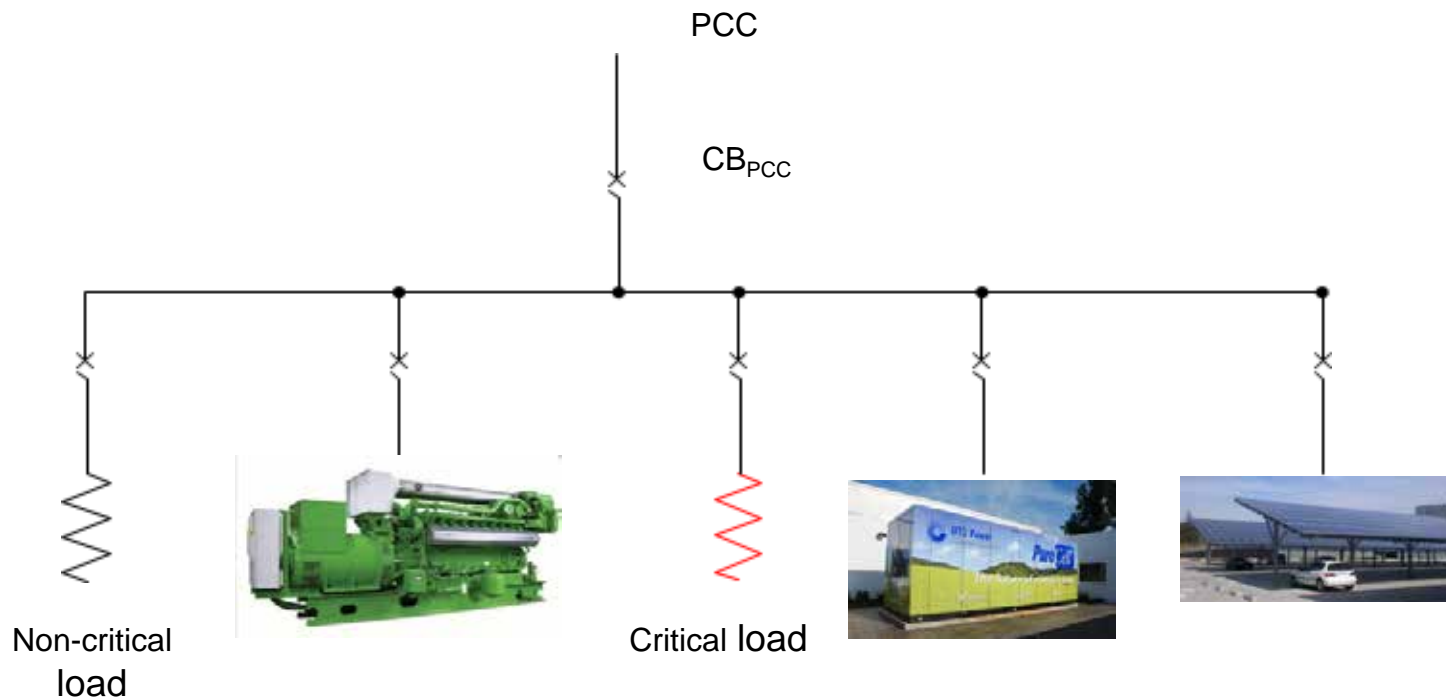
(CHP source: Romelectro)



(PV source: Wikipedia)

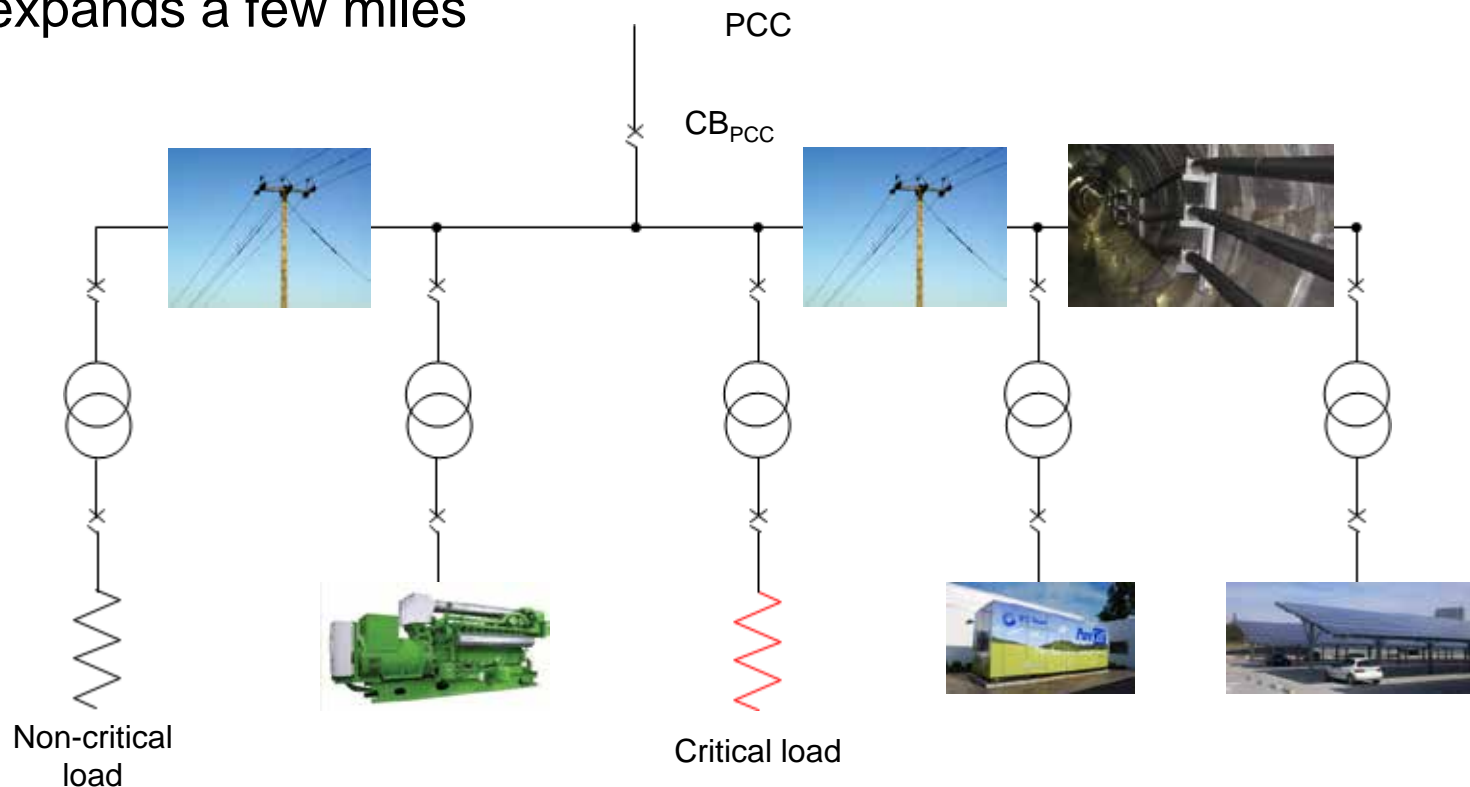
UA Microgrid Vision

- Integration of distributed generation (DG) with power electronic interfaces– in the MVA range
- Grid (islanded mode and grid-connected mode)



UA Microgrid Vision

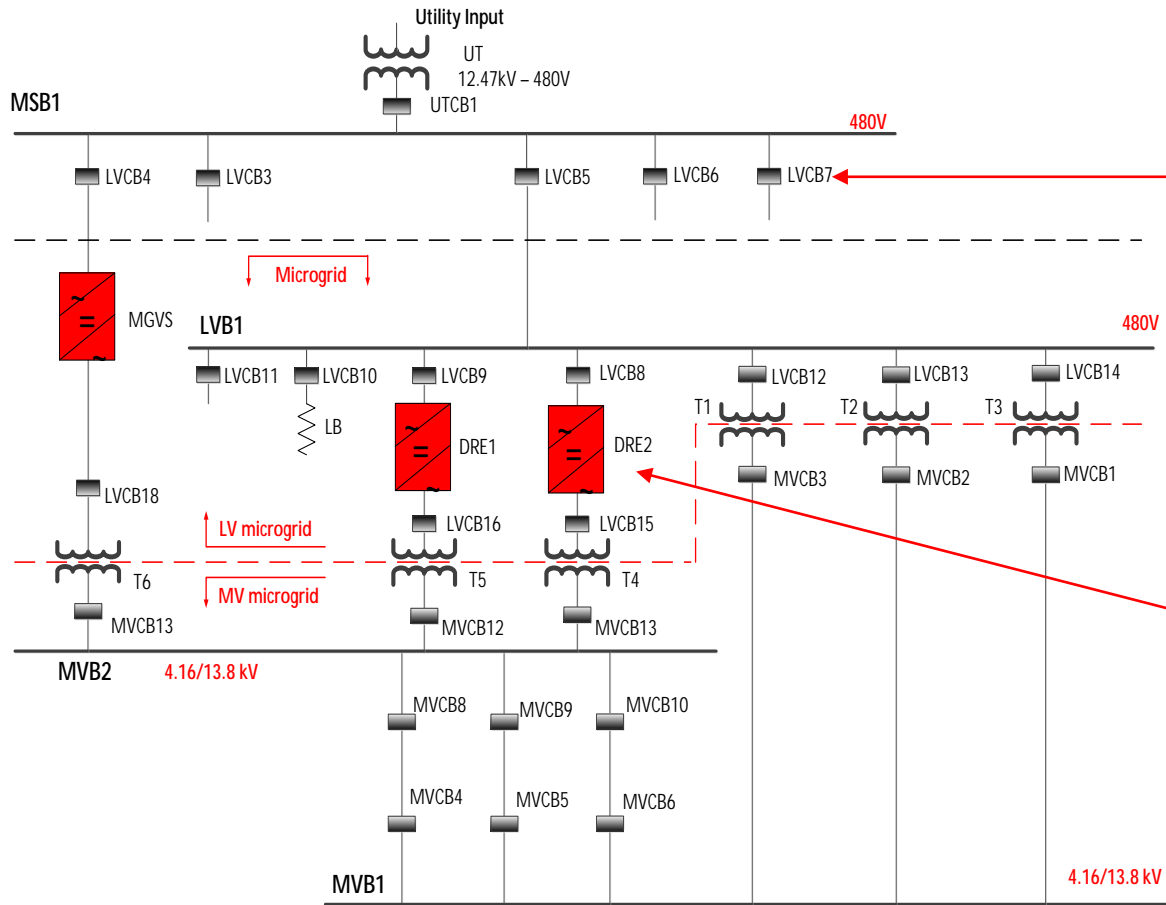
- Integration of distributed generation (DG) with power electronic interfaces— in the MVA range
- Grid (islanded mode and grid-connected mode)
- It expands a few miles



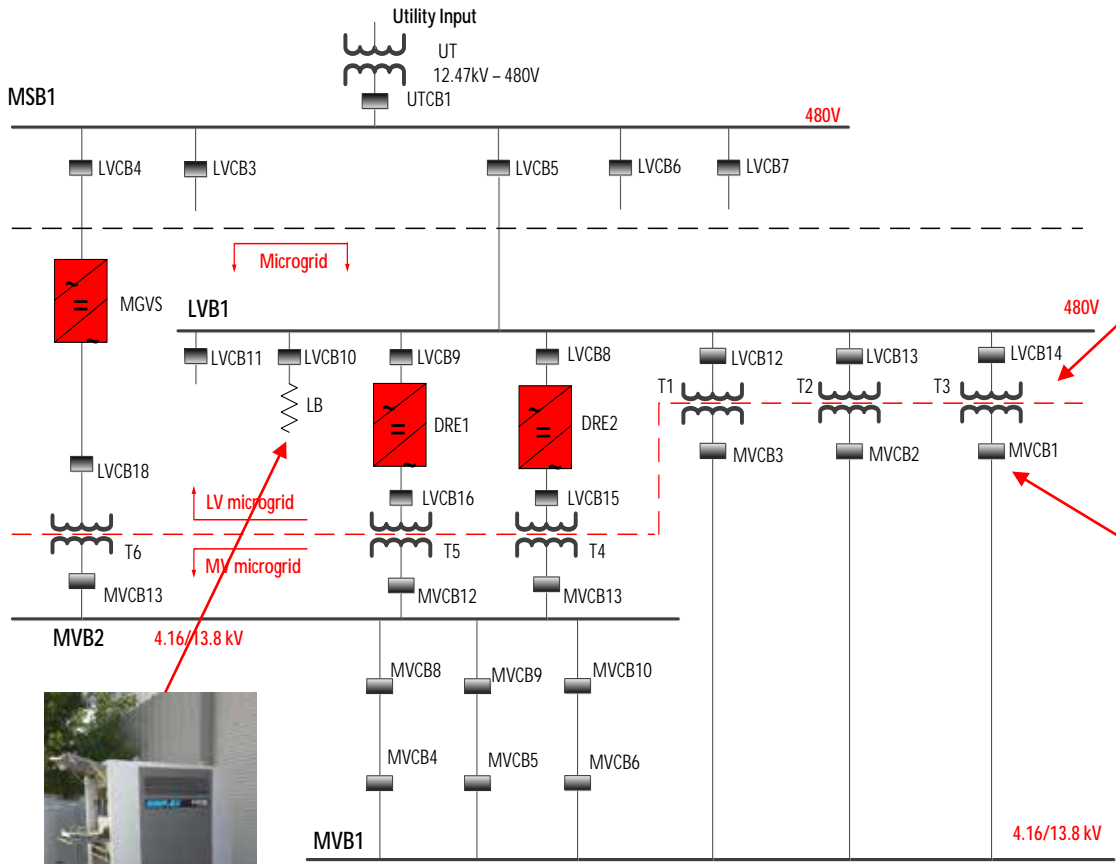
Outline

- Ø Introduction
- Ø UA Microgrid Vision
- Ø **Test Bed System**
- Ø Purposes of the Test Bed
- Ø Hardware Experimental Results
- Ø Conclusions and Future Work

Microgrid Test Bed One-line Diagram



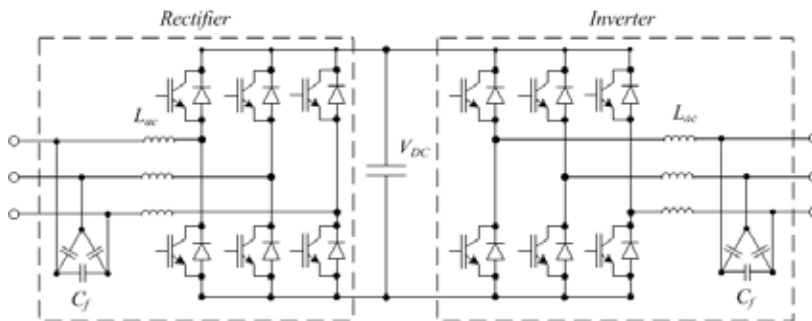
Microgrid Test Bed One-line Diagram



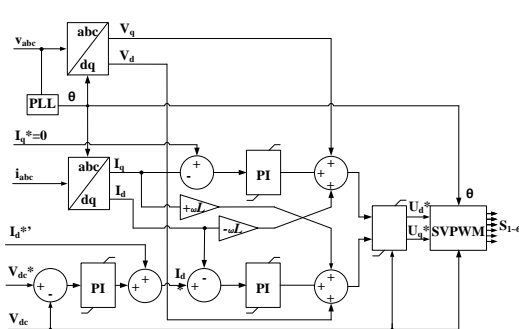
Power Electronic Interfaces

Ø Topology of DG emulator

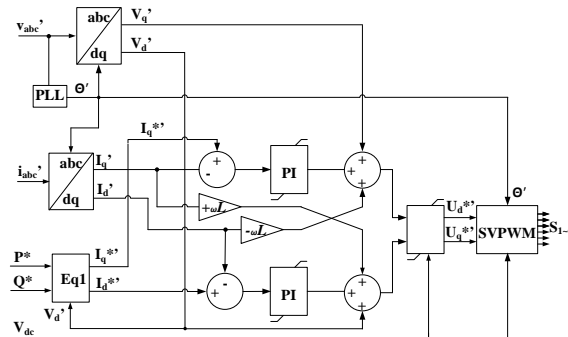
Ø Basic control algorithms



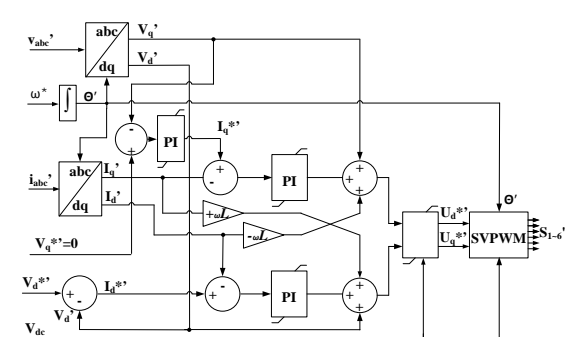
Parameter		Nominal Value
MGVS rated power	S_{MGVS}	0.75 MVA
DRE1 rated power	S_{DRE}	2 MVA
MGVS IGBT switching frequency	$f_{sw-MGVS}$	8 kHz
DRE1 IGBT switching frequency	f_{sw-DRE}	4 kHz
MGVS ac inductor	$L_{ac-MGVS}$	110 μ H (0.135 p.u.)
DRE1 ac inductor	L_{ac-DRE}	20 μ H (0.065 p.u.)
MGVS ac capacitor	C_{f-MGVS}	3 x 1920 μ F
DRE1 ac capacitor	C_{f-DRE}	3 x 768 μ F
DC link capacitor	C_{DC}	46.2 mF
Rated ac voltage	V_{ac}	480 V



AFE rectifier



Grid-feeding inverter



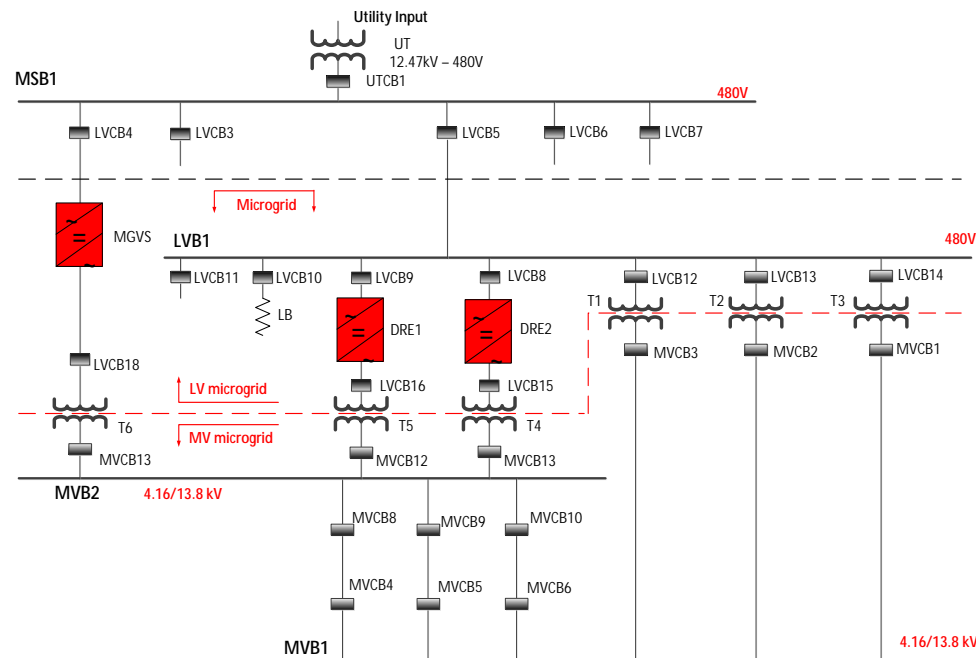
Grid-forming inverter

Outline

- Ø Introduction
- Ø UA Microgrid Vision
- Ø Test Bed System
- Ø **Purposes of the Test Bed**
- Ø Hardware Experimental Results
- Ø Conclusions and Future Work

Purposes of the Test Bed

- Ø Research on microgrids
- Ø Test with regenerative power (at low cost)
 - Commercial products for customers
 - Academic prototypes for students
- Ø Develop control algorithms that are robust against cyber attacks

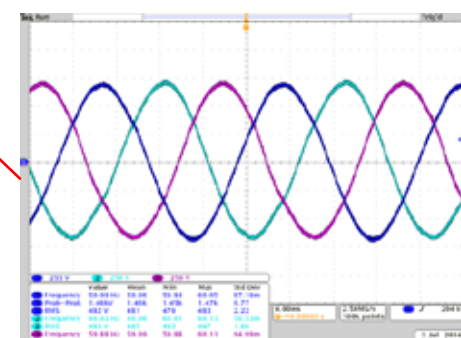
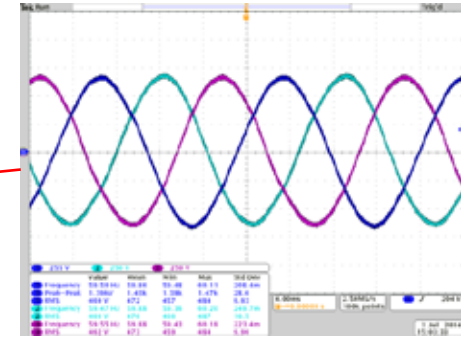
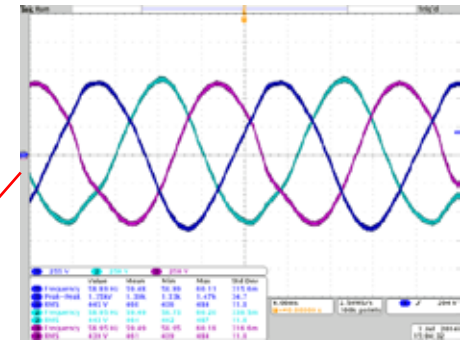
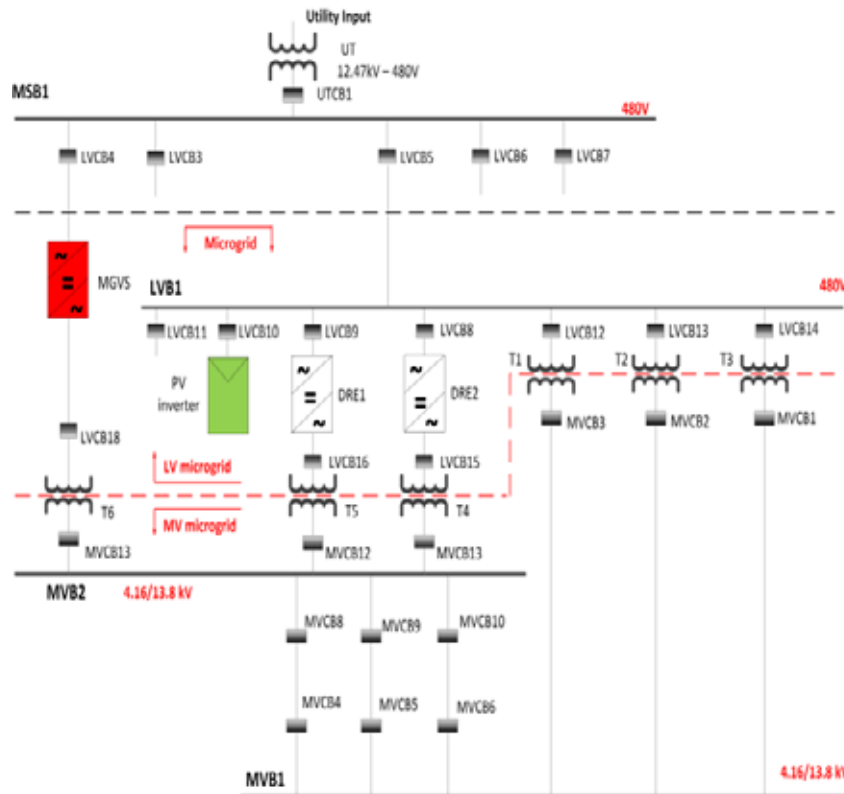


Outline

- Ø Introduction
- Ø UA Microgrid Vision
- Ø Test Bed System
- Ø Purposes of the Test Bed
- Ø **Hardware Experimental Results**
- Ø Conclusions and Future Work

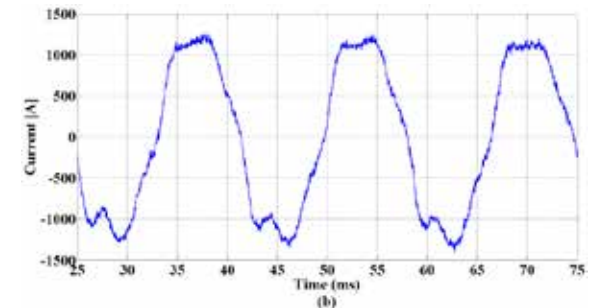
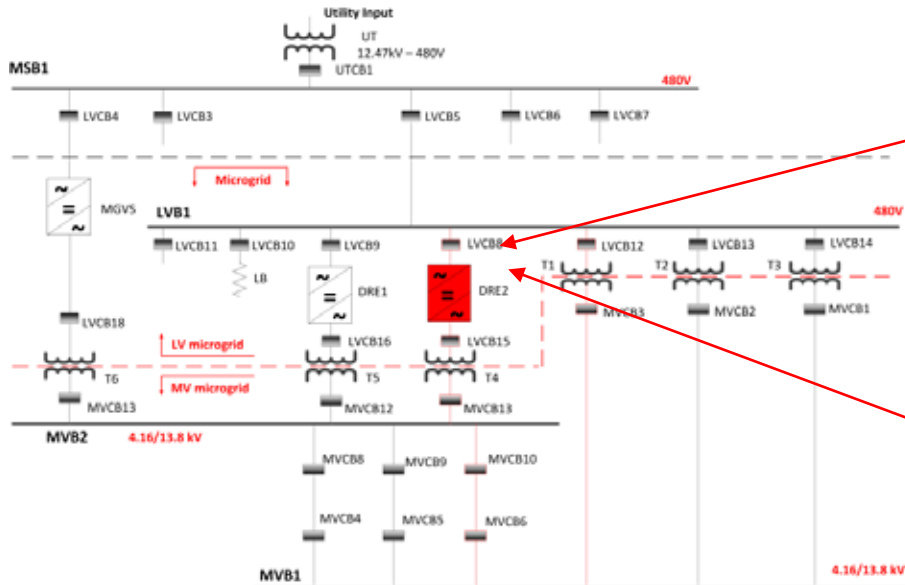
Hardware Experimental Results

⊘ Test commercial products for customers with regenerative power

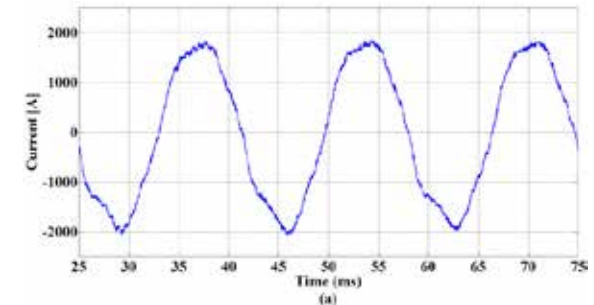


Hardware Experimental Results

∅ DRE regenerates power in grid-connected mode



30% (0.72 kA rms)

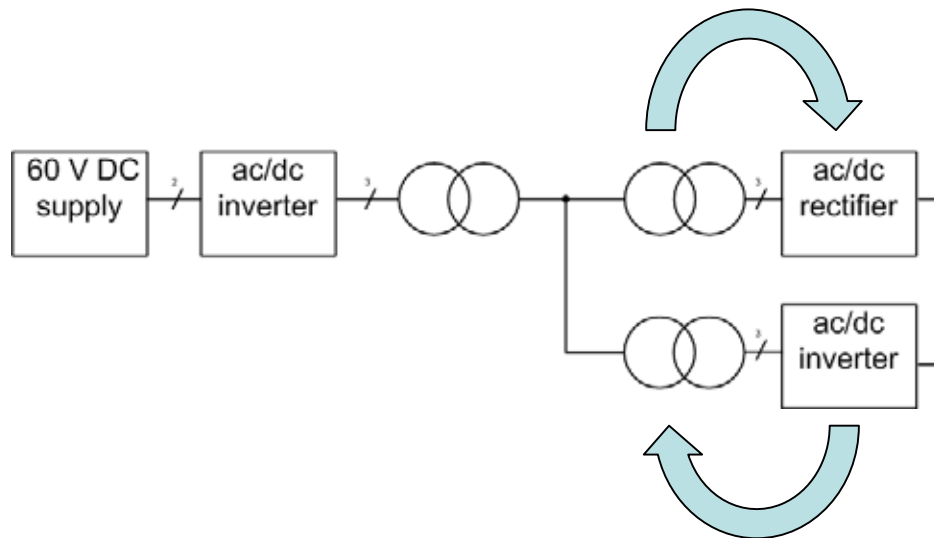


50% (1.2 kA rms)

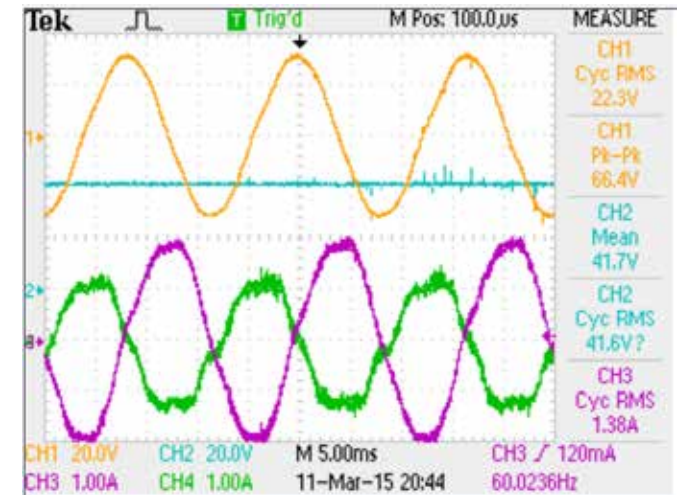
High Power Test Bed Challenges

∅ Safety Environment

- Scaled-down prototype for verifying microgrid topologies and control algorithms
- Test engineer



(a)

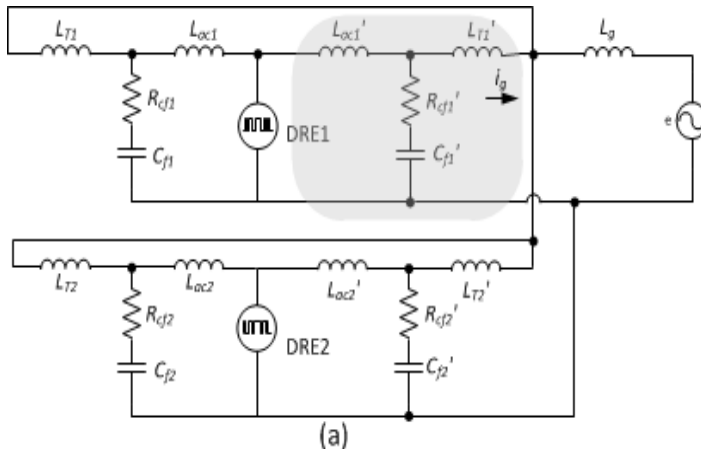


High Power Test Bed Challenges

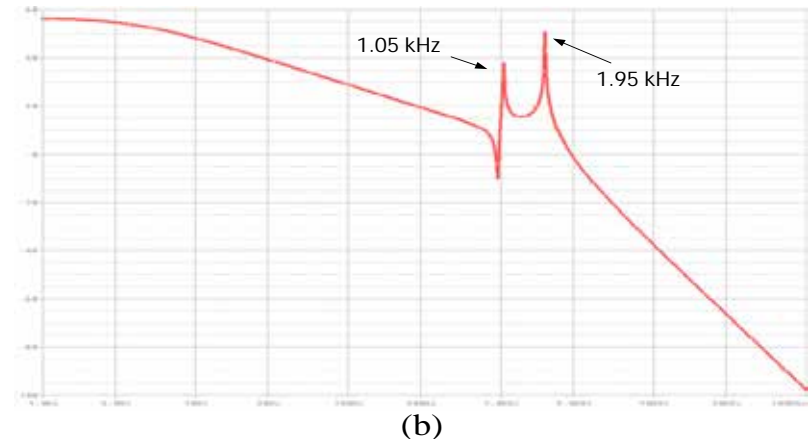
- ∅ Safety Environment
- ∅ Power losses
- ∅ Microgrid stability (resonant propagation)

$$f_{res} = \sqrt{\frac{1}{C_f} \left(\frac{1}{L_{ac}} + \frac{1}{L_g} \right)}$$

$$i_g / U_1 = \frac{(C_f R_{cf} \times s + 1)}{[C_f L_{ac} L_g \times s^3 + C_f R_{cf} (L_{ac} + L_g) \times s^2 + (L_{ac} + L_g) \times s]}$$



Filter equivalent circuit



PSpice™ Bode plot

Outline

- Ø Introduction
- Ø UA Microgrid Vision
- Ø Test Bed System
- Ø Purposes of the Test Bed
- Ø Hardware Experimental Results
- Ø **Conclusions and Future Work**

Conclusions

- ∅ A microgrid laboratory test bed was proposed
- ∅ The preliminary experimental tests (in both grid-connected and island modes)

Future Work

∅ Software developments for future microgrid research

- Droop control
- Active damping
- transition-to-island mode and reconnection mode

∅ Microgrid energy management

ACKNOWLEDGEMENT

∅ The authors are grateful for the financial support from the National Science Foundation Industry/University Cooperative Research Center on Grid-connected Advanced Power Electronics Systems (GRAPES).



GRAPES

GRid-connected Advanced Power Electronic Systems



감사합니다 Natick
Danke Ευχαριστίες Dalu
Thank You Köszönöm
Grazie Tack Obrigado
Спасибо Dank Gracias
谢谢 **Merci** Seé
ありがとう

